

FIG. 1

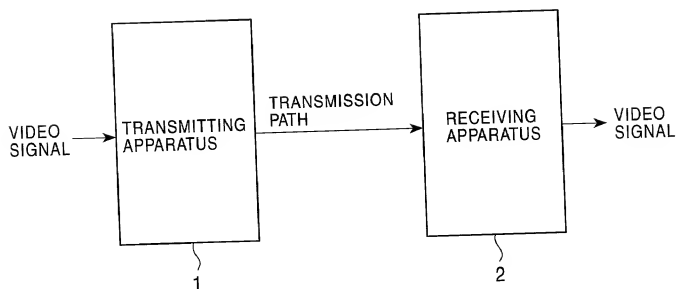


FIG. 2

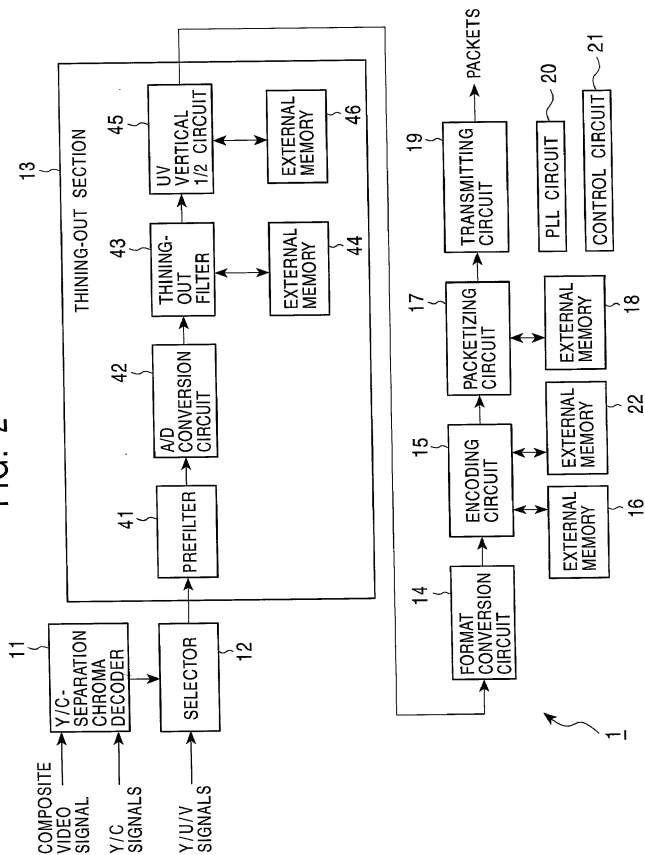


FIG. 3

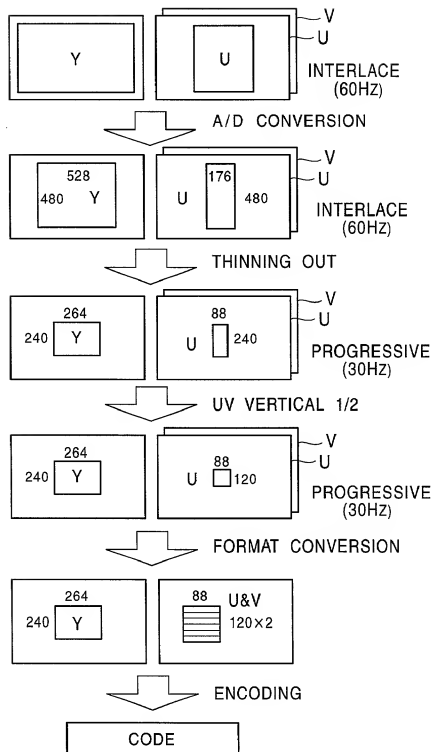


FIG. 4

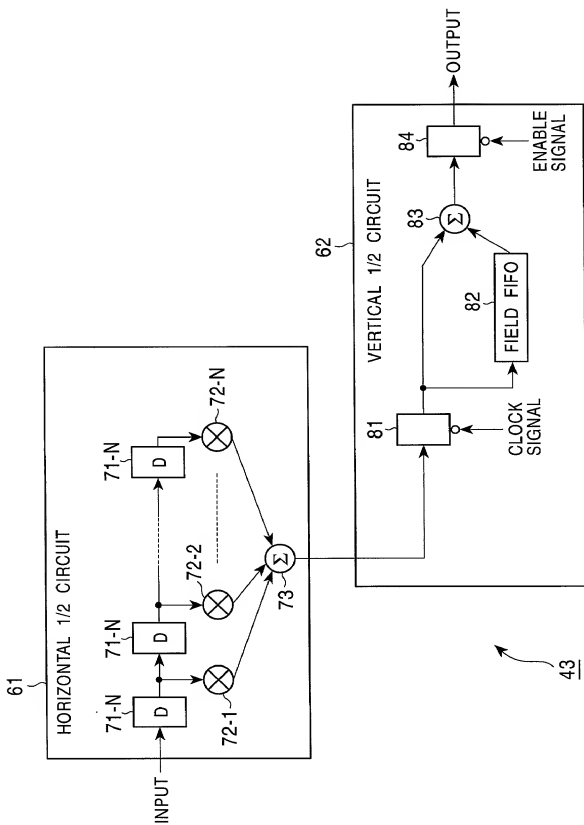


FIG. 5

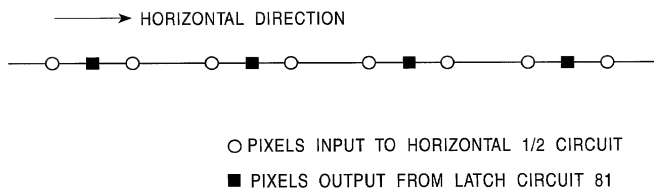
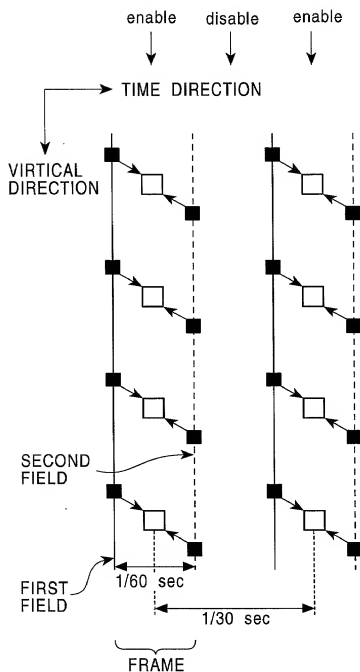


FIG. 6



■ PIXELS OUTPUT FROM LATCH CIRCUIT 81

□ PIXELS OUTPUT FROM LATCH CIRCUIT 84

FIG. 7

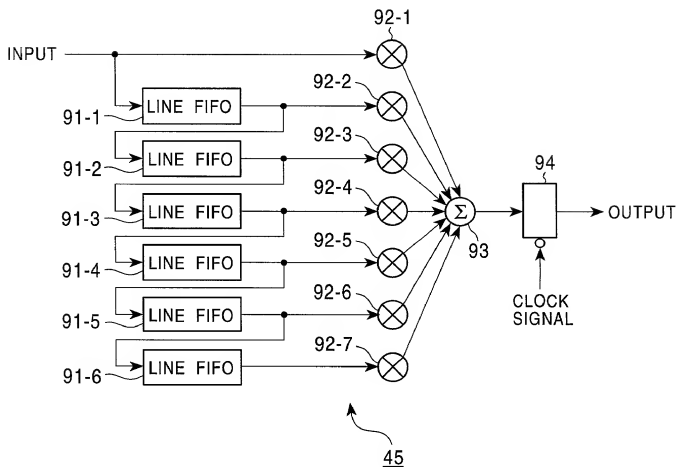


FIG. 8

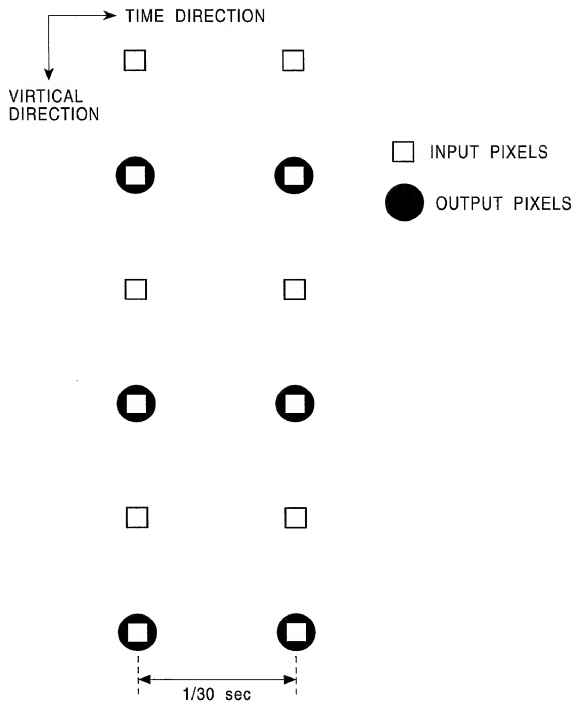




FIG. 9

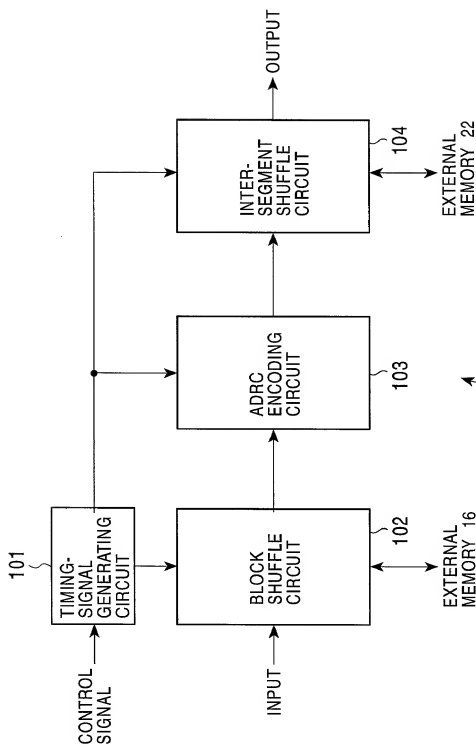


FIG. 10

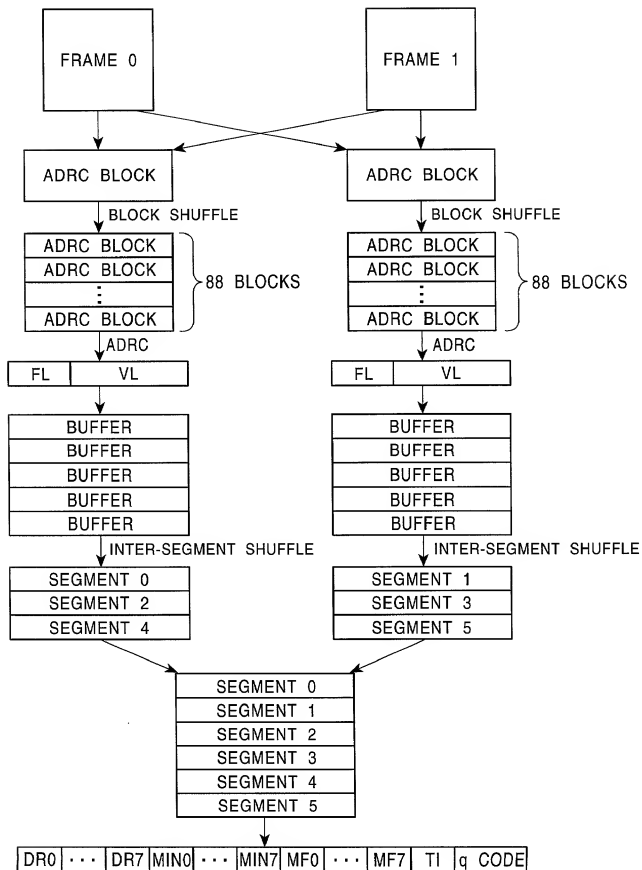


FIG. 11

